

Curriculum Vitae

Personal

Surname: Al-Zubaidi
First name: Yasir Amer Abbas
Address: Kajnag – Selangor - Malaysia
Telephone number: +60107806368
Date of birth: 19 October 1975
Place of birth: Diyala-Iraq
Gender: Male
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Work Experience

- 2000-2003** *Assistance Engineering*
Research Computer Lab. for postgraduate students in Al- Rashed College - Baghdad – Iraq.
- 2003-2005** *Assistance Engineering*
Computer Engineering Department -University of Technology - Baghdad - Iraq.
- 2003-2010** *Monitoring and Evaluation Team Leader (Part Time)*
Danish Refugee Council (DRC) – IRAQ Mission –Diyala Offices.
- 2005-2007** *Assistance Lecturer*
Computer Engineering Department -University of Technology - Baghdad - Iraq.
- 2007-2011** *Assistance Lecturer*
Computer Engineering Department - College of Engineering -University of Diyala- Diyala - Iraq.
- 2011- Present** *PhD Candidate.*
Department of Electronic and communication Engineering- College of Engineering –Universiti Tenaga Nasional- Selangor –Malaysia.

Academic Qualifications:

- M.Sc Computer engineering from University of Technology – Al-Rasheed college of engineering and science, Baghdad, Iraq.(graduation Year 2005).
- B.Sc Computer Engineering from University of Technology – Al-Rasheed college of engineering and science, Baghdad, Iraq.(graduation Year 2000).

Research Areas/ Interests:

- Embedded system design, Microprocessor design, FPGA, Heterogeneous Multi-core Processors on CPU/FPGA Architectures, Embedded Crypto Design, Security in wireless communication, embedded image processing.

Educational and Training:

- Design ALU and Array division and multiplication 2005.
- Workshop in Protection in the context of Humanitarian Assistance in Iraq. Amman- Jordan-May 2005.
- Project Cycle Management and Proposal Writing. From The Queen Zein Al Sharaf Institute for Development, in Amman- Jordan-Aug.-2005.
- Types of Study, university training and computer educational. From University of technology- center of continues study- Baghdad- Iraq- Oct.- 2005.
- Certificated in Computer proficiency from computer and internet center- Diyala University – Diyala – Iraq.

Publication:

1. Yasir Amer Abbas, Razali Jidin “Reconfigurable Design of Heterogenous Multiprocessors using FPGA Platforms: A Review” NatGrad2012, UNITEN, 2012.
2. Yasir Amer Abbas, Razali Jidin, Pooria Varahram “Evaluation of Reconfigurable Technique for Heterogeneous Multi-core Processors with FPGA Platform” Image Processing, Image Analysis and Real-Time Imaging (IPIARTI) Symposium 2013, I EEE Signal Processing Society (Malaysia Section), 2013.
3. Haider Ismael Shahadi, Razali Jidin, Wong Hung Way and Yasir Amer Abbas “Efficient FPGA Architecture for Dual Mode Integer Haar Lifting Wavelet Transform Core” Journal of Applied Sciences, Page 5, Volume 14,2014.
4. Saad Al-Azawi, Yasir Amer Abbas, Razali Jidin,” Low Complexity Multidimensional CDF 5/3 DWT Architecture”9th International Symposium on Communication Systems, Networks & Digital Sign (CSNDSP), IEEE Explore, July 2014.

Main Courses:

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|---------------------------|------------------------------|
| - Computer Architecture. | - Microprocessor |
| - PC Interface | - Data communication. |
| - Digital Circuit Design. | - Digital Signal Processing. |
| - Electronic. | - Computer Network. |
| - Operating System. | -Image Processing. |

Computer Experience:

Embedded system design, VHDL, C, C++,Matlab, Database design, Installation and administration of local Area Network.