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**Published Researches:**

1. Design and Implementation of Power Station Event Processor, Journal of engineering, 2001.
2. Event Processor In National Control Center SCADA System, Second national conference on computer, communication and control system, 2001.
3. ECG Slantlet Transform with FPGA design, Journal of engineering, 2010.
4. Implementation of Root Finding Algorithm of Minimum Phase Filter using VHDL, Journal of engineering, 2011.
5. OFDM PAPR reduction for image transmission using improved tone reservation, International Journal of Electrical and Computer Engineering (IJECE), 2021.
6. A partial image encryption scheme based on DWT and texture segmentation, Cogent Engineering, 2022.
7. Variable length error correcting code for image in OFDM and PAPR reduction, Indonesian Journal of Electrical Engineering and Computer Science, **2022**.

**Certificate:** Master in Electronics and Communication Engineering/University of Baghdad/College of Engineering/1999.

**Research Specialization:** Electronic and Communication/Image processing and security system.